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# Question Paper Code: 50544

## B.E./B.Tech. DEGREE EXAMINATIONS, APRIL/MAY 2023

Sixth/Seventh/Eighth Semester

Electrical and Electronics Engineering

### EE 8691 - EMBEDDED SYSTEMS

(Common to Electronics and Instrumentation Engineering / Instrumentation and Control Engineering)

(Regulations 2017)

Time: Three hours

Maximum: 100 marks

#### Answer ALL questions.

#### PART A — $(10 \times 2 = 20 \text{ marks})$

- 1. Write three key functional requirements of an embedded system.
- 2. Distinguish between CISC and RISC.
- 3. List down at least three important features of SPI.
- 4. What is the role of device drivers in an embedded system?
- 5. What is an object oriented model?
- 6. What are the processes involved in co-design?
- 7. Define threading and multi-threading?
- 8. What is priority inversion?
- 9. What is a prototype?
- 10. What are the events involved in smart card application?

PART B — 
$$(5 \times 13 = 65 \text{ marks})$$

11. (a) With a neat diagram explain the working of direct memory access architecture and timing diagram. (13)

Or

(b) Discuss real time clock and target hardware debugging.

(13)

| (7+6)                                   | Explain the following communication protocols in detail: I <sup>2</sup> C BUS.   | (a) | 12. |
|---|--|-----|-----|
|   | $\operatorname{Or}$  |     |     |
| (13)                                    | Explain the I/O device ports and their characteristics.  | (b) |     |
| (13)                                    | Explain sequential programming model with an example.  | (a) | 13. |
| ware–hardware<br>(7)                    | (i) Highlight the issues and challenges in soft<br>Co-design.  | (b) |     |
| (6)                                     | (ii) Discuss the different phases of EDLC in detail.   |     |     |
| (13)                                    | Explain inter process communication in detail.  Or   | (a) | 14. |
| (13)                                    | Explain how interrupt routines are handled by RTOS.  | (b) |     |
| r with suitable (13)                    | sketches.  | (a) | 15. |
|   | Answer Aldr questions  |     |     |
| ayment machine                          | Explain embedded design concept used in debit card pawith necessary diagrams.  | (b) |     |
| 2. Distinguis                           | PART C — (1 × 15 = 15 marks)   |     |     |
| abedded system-                         | Consider the modern washing machine which is an emenabled product.   | (a) | 16. |
| and explain how<br>the necessary<br>(8) | (i) Draw the functional architecture of the system a software is interacting the hardware (include diagram and flow chart) |     |     |
|   | (ii) Analyze the following cases: washing and spinni load and overload.  |     |     |
| saitadW .e                              | Or   |     |     |
| warning system<br>(15)                  | Explain state machine model for an automatic seat belt with necessary diagrams and flowcharts.                             | (b) |     |
|   | PART B — (5 × 12 = 65 marks)   |     |     |